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APPLICANTS

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** CONTINUING DATA *****

This appln claims benefit of 60/422,117 10/30/2002

** FOREIGN APPLICATIONS *****

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 ** 02/02/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature: <i>[Signature]</i> Initials: <i>[Initials]</i>	STATE OR COUNTRY CA	SHEETS DRAWING 30	TOTAL CLAIMS 82	INDEPENDENT CLAIMS 6
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ADDRESS

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TITLE

Method and apparatus for testing asynchronous set/reset faults in a scan-based integrated circuit

FILING FEE

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